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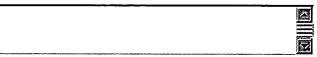
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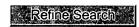
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André Meyer July 1995

ACM SIGCHI Bulletin, Volume 27 Issue 3

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Modeling the Power Consumption of Audio Signal Processing Computations Using Customi: Roger Chamberlain, Eric Hemmeter, Robert Morley, Jason White March 2003 Proceedings of the 36th annual symposium on Simulation

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This paper explores the impact that numerical representation has on the power consumption of auc design goal. We investigate twoaspects of this problem. First, we evaluate the validity ofusing sign consumption of multiply-accumulateoperations for seve ...

Keywords: audio signal processing, power consumption, numerical representation

An architecture for voice dialog systems based on prolog-style theorem proving Ronnie W. Smith, Alan W. Biermann, D. Richard Hipp

September 1995

Computational Linguistics, Volume 21 Issue 3

Full text available: pdf(2.76 MB) Publisher Site

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A pragmatic architecture for voice dialog machines aimed at the equipment repair problem has beinclude:(1) problem solving to achieve a target goal(2) the ability to carry out subdialogs to achiev verbal exchanges and to inhibit unnecessary ones(...

Power minimization in IC design: principles and applications

Massoud Pedram January 1996

ACM Transactions on Design Automation of Electronic Systems (TOI

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Low power has emerged as a principal theme in today's electronics industry. The need for low pow

an in-depth survey of CAD methodologies and techniques for designing low power digital CMOS cir abstraction. It reviews some of the techniques and tool ...

Keywords: CMOS circuits, adiabatic circuits, computer-aided design of VLSI, dynamic power dissi and estimation, power management, power minimization and management, probabilistic analysis, system design

The CRITTER system: Automated critiquing of digital circuit designs

Van E. Kelly June 1984

Proceedings of the 21st conference on Design automation

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Additional Information: f

CRITTER is an exploratory prototype design aid, built using Artificial Intelligence ("Expert Systems robustness, and circuit sensitivity to changes in device parameters. Its non-procedural represental ordinary m ...

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Peter G. Neumann

January 1996 ACM SIGSOFT Software Engineering Notes, Volume 21 Issue 1

Full text available: pdf(2.54 MB)

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The FINITE STRING Newsletter: Abstracts of current literature

Computational Linguistics Staff

January 1987 Computational Linguistics, Volume 13 Issue 1-2

Full text available: pdf(6.15 MB) Publisher Site 1

Additional Information: full citation

Columns: Risks to the public in computers and related systems

Peter G. Neumann

January 2001 ACM SIGSOFT Software Engineering Notes, Volume 26 Issue 1

Full text available: pdf(3.24 MB)

Additional Information: full citation

A reconfigurable dual output low power digital PWM power converter

Abram Dancy, Anantha Chandrakasan

August 1998

Proceedings of the 1998 international symposium on Low power ele

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Most work to date on power reduction has focused at the component level, not at the system level of resources, an environmental workload specification, and a power management policy, which ser power dissipation. Accompanying th ...

10 Systems semantics: principles, applications, and implementation

Ray Boute January 1988

Full text available: pdf(2.71 MB)

ACM Transacti ns on Programming Languages and Systems (TOPLA Additional Information: f

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11 Overview of the power minimization techniques employed in the IBM PowerPC 4xx embedden Anthony Correale

April 1995 Proceedings f the 1995 international symp sium n L w power design

Full text available: pdf(44.29 KB)

Additional Information: full citation, references, citings, index terms

12 Abstract state machines capture parallel algorithms

Andreas Blass, Yuri Gurevich

October 2003

ACM Transactions on Computational Logic (TOCL), Volume 4 Issue 4

Full text available: pdf(610.28 KB)

Additional Information: full

We give an axiomatic description of parallel, synchronous algorithms. Our main result is that even

Keywords: ASM thesis, Parallel algorithm, abstract state machine, postulates for parallel compute

13 Development and application of NASA's first standard spacecraft computer

Charles E. Trevathan, Thomas D. Taylor, Raymond G. Hartenstein, Ann C. Merwarth, William N. Stev September 1984 Communications of the ACM, Volume 27 Issue 9

Full text available: pdf(1.26 MB)

Additional Information: full citation, abstract, c

To provide the autonomy needed by low, earth-orbiting satellites, NASA's first standard on-board

Keywords: PASS, avionics system

14 Object-oriented logical specification of time-critical systems

Angelo Morzenti, Pierluigi San Pietro

January 1994

ACM Transactions on Software Engineering and Methodology (TOSE

Full text available: pdf(3.05 MB)

Additional Information: f

We define TRIO+, an object-oriented logical language for modular system specification. TRIO+ is I that provides an effective support to a variety of validation activities, like specification testing, sim

Keywords: first-order logic, formal specifications, model-theoretic semantics, object-oriented me

15 New models and architectures: Spatial computation

Mihai Budiu, Girish Venkataramani, Tiberiu Chelcea, Seth Copen Goldstein

October 2004

Proceedings of the 11th international conference on Architectural s

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This paper describes a computer architecture, Spatial Computation (SC), which is based on the tra distributed, with no centralized control. SC circuits are optimized for wires at the expense of comp assumption that computation is cheaper than co ...

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Formal verification in hardware design: a survey

Christoph Kern, Mark R. Greenstreet

April 1999 ACM Transacti ns on Design Automation f Electronic Systems (TOI

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In recent years, formal methods have emerged as an alternative approach to ensuring the quality and testing. There are two main aspects to the application of formal methods in a design process: about the relationship between a spec ...

Keywords: case studies, formal methods, formal verification, hardware verification, language cor

17 A true single-phase 8-bit adiabatic multiplier

Suhwan Kim, Conrad H. Ziesler, Marios C. Papaefthymiou

June 2001

Proceedings of the 38th conference on Design automation

Full text available: pdf(647.64 KB)

Additional Information: f

This paper presents the design and evaluation of an 8-bit adiabatic multiplier. Both the multiplier (adiabatic circuitry via a sinusoidal power-clock waveform that is generated on-chip. In HSPICE sim dissipation of the multiplier co ...

Keywords: CMOS, SCAL, SCAL-D, VLSI, adiabatic logic, clock generator, dynamic logic, low energy

18 Effects of variable initiative on linguistic behavior in human-computer spoken natural languar

Ronnie W. Smith, Steven A. Gordon

March 1997

Computational Linguistics, Volume 23 Issue 1

Full text available: pdf(1.99 MB) Publisher Site

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This paper presents an analysis of the dialogue structure of actual human-computer interactions. organized around the paradigm of the Missing Axiom Theory for language use. Results about utter frequency of linguistic control shifts, and frequenc ...

19 A static power model for architects

J. Adam Butts, Gurindar S. Sohi

December 2000 Proceedings of the 33rd annual ACM/IEEE international symposium on Micro

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Additional Information: full citation, references, citings, index

20 Razor: A Low-Power Pipeline Based on Circuit-Level Timing Speculation

Dan Ernst, Nam Sung Kim, Shidhartha Das, Sanjay Pant, Rajeev Rao, Toan Pham, Conrad Ziesler, Di December 2003 Proceedings of the 36th Annual IEEE/ACM International Symposiun

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